

ENA EREC G99/1-4:2019

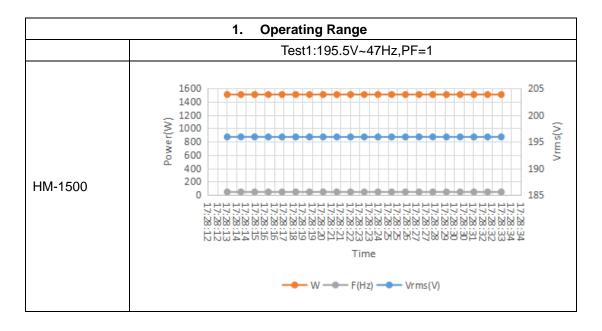
Type Test reference number		HM-1500,HM-1200				
Generating Unit technology		Photovoltaic Microinverter				
System Supplier na	me	Hoymiles F	Power Electronic	s Inc.		
Address		_	No.18 Kangjing Road, Hangzhou, Zhejiang Province, P.R. China.			
Tel	LOG E71 200EG10		Fax	106 E71 200E6127		
Tel	+86 571 2805610	<u> </u>	гах	+86 571 28056137		
E:mail	zhangxingyao@h: om	zconverter.c	Web site	www.hoymiles.com		
Registered	1.5/1.2 per Unit	kW single p	phase, single, split or three phase system			
Capacity, use	NA	kW three pl	nase			
separate sheet if	NA	kW two pha	ses in three phas	se system		
more than one	A D A					
connection option.	N`A	KVV two pna	ses split phase s	system		

Manufacturer compliance declaration. - I certify that all products supplied by the company with the above Type Tested Manufacturer's reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site Modifications are required to ensure that the product meets all the requirements of EREC G99.

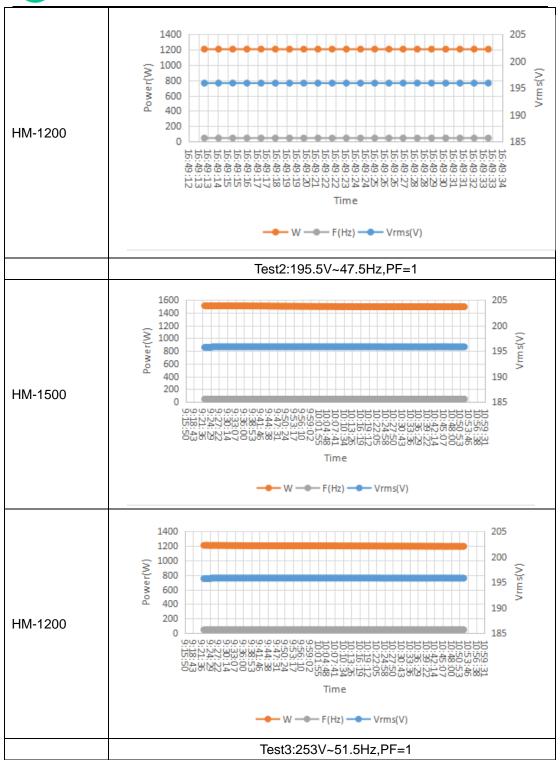
Signed	On behalf of	Hoymiles Power Electronics Inc.
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Note that testing can be done by the Manufacturer of an individual component or by an external test house.

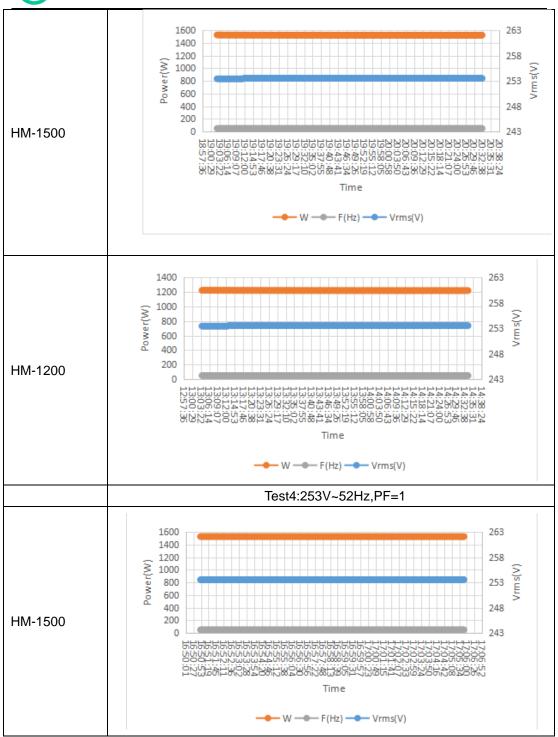
Where parts of the testing are carried out by persons or organisations other than the Manufacturer then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.



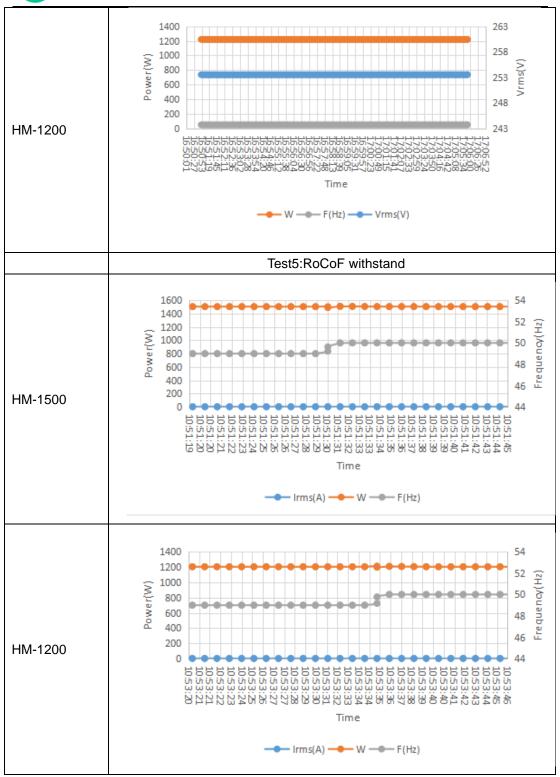
H hoymiles 而适



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H hoymiles 市伍



2. Power Quality - Harmonic Generation Generating Unit tested to BS EN 61000-3-12						
Generating Unit rating per phase (rpp)	1.5	kW	Harmonic % =Measured Value (Amps) x 23/rating per phase (kVA)			



					ı	
Harmo nic	At 45-55% o	f rated output	100% of ra	ated output		BS EN 0-3-12
1.110	Measured		Measured		0100	0 0 12
	Value MV		Value MV			
		%		%	1 phase	3 phase
	in Amps		in Amps			
2	0.0111	0.0284	0.0032	0.0484	8%	8%
					04.00/	Not
3	0.0421	0.1076	0.0086	0.1314	21.6%	stated
4	0.0084	0.0214	0.0014	0.0209	4%	4%
5	0.0241	0.0617	0.0035	0.0532	10.7%	10.7%
6	0.0035	0.0090	0.001	0.0155	2.67%	2.67%
7	0.0089	0.0227	0.0025	0.0387	7.2%	7.2%
8	0.0031	0.0080	0.0009	0.0137	2%	2%
0					2.00/	Not
9	0.0096	0.0245	0.0017	0.0263	3.8%	stated
10	0.0033	0.0085	0.0008	0.0119	1.6%	1.6%
11	0.0054	0.0137	0.0015	0.0232	3.1%	3.1%
12	0.0025	0.0065	0.0004	0.0059	1.33%	1.33%
13	0.0033	0.0085	0.001	0.0145	2%	2%
THD		2.6029		2.8401	23%	13%
PWHD		4.3901		5.4591	23%	22%
system s	size is scalable	this is the system	em size tested l	by 1 Unit (HM-1	1500).	

3. Power Quality. Voltage fluctuations and Flicker									
Test to BS EN 61000-3-11									
	Startin	g		Stoppi	ng		Running		
	dmax [%]	dc [%]	d(t) [%]	dmax [%]	dc [%]	d(t) [%]	Pst	Plt 2 hours	
Measured Values									
at	0.1	0	0	0.1	0	0	0.064	0.064	
test impedance									
Normalised to standard impedance	0.1	0	0	0.1	0	0	0.064	0.064	
Normalised to required maximum impedance	0.1	0	0	0.1	0	0	0.064	0.064	
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1	0.65	
Test impedance	R	0.4		Ω	ΧI	0.25		Ω	



Standard impedance	R	0.24* 0.4^	Ω	ΧI	0.15* 0.25^	Ω
Maximum impedance	R	0.4	Ω	XI	0.25	Ω
Test start date	Test 2020-07-31 end 2020-07-31 date				2020-07-31	
Test location		SHANGHAI TESTING & INSPECTION INSTITUTE FOR ELECTRICAL EQUIPMENT CO., LTD.				

4. Power quality. DC in					
Test power level	10%	55%	100%		
Recorded value(mA)	1.146	4.974	2.869		
as % of rated AC	0.039/	0.000/	0.0459/		
current	0.02%	0.08%	0.045%		
Limit	0.25%	0.25%	0.25%		

5. Power Quality. Power factor							
	216.2V	230V	253V	Measured at three voltage levels			
Measured value	0.9985	0.9976	0.9955	and at full output. Voltage to be maintained within ±1.5% of the			
Limit	>0.95	>0.95	>0.95	stated level during the test.			

6. Protection. Frequency tests								
Function	Setting		Trip test		"No trip tests"			
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip		
U/F stage 1	47.5Hz	20s	47.5Hz	20.1s	47.7Hz/30s	Confirmed		
U/F stage 2	47Hz	0.5s	47Hz	0.53s	47.2Hz/19.5s	Confirmed		
					46.8Hz/0.45s	Confirmed		
O/F stage 1	52Hz	0.5s	52Hz	0.53s	51.8Hz/120s	Confirmed		
					52.2Hz/0.45s	Confirmed		

7. Protection. Voltage tests								
Function	Setting		Trip test		"No trip tests"			
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip		
U/V stage 2	184V	2.5s	183.6V	2.52s	188V/5.0s	Confirmed		
					180V/2.45s	Confirmed		
OV stage 1	262.2V	1.0s	262.6V	1.03s	258.2V/5.0s	Confirmed		
O/V stage 2	273.7V	0.5s	274.2V	0.52s	269.7V/0.95s	Confirmed		
					277.7V/0.45s	Confirmed		



8. Power Park Modules - Protection - Loss of Mains test								
Note: Inverter tested according to BS EN 62116.								
Toot Dower and	33%	66%	100%	33%	66%	100%		
Test Power and imbalance	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P		
imbalance	Test 22	Test 12	Test 5	Test 31	Test 21	Test 10		
Trip time. Limit is 0.5s	84.6ms	178.3ms	232.4ms	86.5ms	174.6ms	233.5ms		

9. Loss of Mains Protection, Vector Shift Stability test and RoCoF Stability test								
	Start Frequency	Change	End Frequency	Confirm no trip				
Positive Vector Shift	49.5Hz	+50 degrees		Confirmed				
Negative Vector Shift	50.5Hz	- 50 degrees		Confirmed				
	Ramp range	Test frequency ramp	Test Duration	Confirm no trip				
Positive Frequency drift	49Hz to 51Hz	+0.95Hz/sec	2.1s	Confirmed				
Negative Frequency drift	51Hz to 49Hz	-0.95Hz/sec	2.1s	Confirmed				

10. Limited Frequency Sensitive Mode - Over frequency test				
Active Power resp				
frequency injection	N			
Test sequence	Measured	Frequency	Primary	Active
at Registered	Active		Power	Power
Capacity >80	Power Output		Source	Gradient
%				
Step a) 50.00Hz	1499.25 W	50Hz		-
±0.01Hz	1499.29 VV	30112		
Step b) 50.45Hz	1484.50 W	50.45Hz		-
±0.01Hz	1404.50 W	30.43112		
Step c) 50.70Hz	1412.00W	50.7Hz		-
±0.10Hz	1412.0000	30.7112		
Step d) 51.15Hz	1280.63W	51.15Hz		-
±0.05Hz	1200.03	31.13112		
Step e) 50.70Hz	1412.25W	50.7Hz		-
±0.10Hz	1412.23	30.7112		
Step f) 50.45Hz	1484.75W	50.45Hz		-
±0.05Hz	1404.75	30.43112		
Step g) 50.00Hz	1498.25W	50Hz		-
±0.01Hz	1490.23	30112		
Test sequence	Measured	Frequency	Primary	Active
at Registered	Active		Power	Power
Capacity	Power Output		Source	Gradient
40%~60%				
Step a) 50.00Hz	766.50W	50Hz		-



±0.01Hz		
±0.01112		
Step b) 50.45Hz	750 0514	EO 4511-
±0.05Hz	759.25W	50.45Hz
Step c) 50.70Hz	700 101//	E0.7U-
±0.10Hz	722.13W	50.7Hz
Step d) 51.15Hz	CEC EO W	E4 45U-
±0.05Hz	656.50 W	51.15Hz
Step e) 50.70Hz	704 99\\/	50.7U-
±0.10Hz	721.88W	50.7Hz

11. Protection. Re-connection timer					
Test should prove that the reconnection sequence starts in no less than 20s for restoration					
of voltage and frequency to within the stage 1 settings of table 10.5.7.1					
Time delay	Measured	Checks on no reconnection when voltage or frequency is			
setting	delay	brought to just outside stage 1 limits of table 10.5.7.1.			
20s	30s	At 266.2V	At 180V	At 47.4Hz	At 52.1Hz
Confirmation that the					
Generating Unit does not		Confirmed	Confirmed	Confirmed	Confirmed
re-connect.					

12. Fault level contribution					
For machines with electro-magnetic output			For Inverter output		
Parameter	Symbol	Value	Time after fault	Volts	Amps
Peak Short Circuit current	ip	N/A	20ms	15.4V	0.421A
Initial Value of aperiodic current	А	N/A	100ms	7.57V	0.176A
Initial symmetrical short-circuit current*	lk	N/A	250ms	6.28V	0.126A
Decaying (aperiodic) component of short circuit current*	iDC	N/A	500ms	5.93V	0.117A
Reactance/Re sistance Ratio of source*	X/R	N/A	Time to trip	0.0039s	(in seconds)

For rotating machines and linear piston machines the test should produce a 0s - 2s plot of the short circuit current as seen at the Generating Unit terminals.



* Values for these parameters should be provided where the short circuit duration is sufficiently long to enable interpolation of the plot

13. Self-Monitoring solid st	ate switching	Yes/or NA
It has been verified that in the event of the solid disconnect the Generating Unit , the voltage of switching device is reduced to a value below 5	n the output side of the	N/A

14. Wiring functional tests: If required by para 15.2.1		
Confirm that the relevant test schedule is attached (tests to be undertaken at		
time of commissioning).	N/A	

15. Logic interface (input port)	Yes/or NA
Confirm that an input port is provided and can be used to shut down the	
module.	Yes